

# Data Port EMI Filter Array with ESD Protection

CM1454

#### **Features**

- Optiguard<sup>™</sup> coated for improved reliability
- Eight channels of EMI filtering
- Three C-L-C filters with ESD protection for stereo speaker port
- Five C-R-C filters with ESD protection for microphone and data ports
- ±15kV ESD protection on each channel (IEC 61000-4-2 Level 4, contact discharge)
- ±30kV ESD protection on each channel (HBM)
- Chip Scale Package (CSP) features extremely low parasitic inductance for optimum filter and ESD performance
- 20 bump, 3.960mm x 1.586mm footprint CSP
- RoHS compliant (lead-free)

## **Applications**

- Combination I/O data port that has I/Os for data, microphone and speaker
- I/O port protection for mobile handsets, notebook computers, PDAs etc
- "EMI filtering for data ports in cell phones, PDAs or notebook computers
- Wireless Handsets
- Handheld PCs / PDAs

### **Product Description**

The CM1454 is an EMI filter array with ESD protection in a CSP form factor for the data port of a mobile handset. The CM1454-08 is configured in an 8 channel format and combines both resistor-capacitor (R-C) and inductor-capacitor (L-C) filters in the chip. There are five C-R-C filters with component values of 30pF-100 -30pF which are used for the microphone and data ports. There are also three C-L-C filters with values of 80pF-3nH-80pF which are designed for the stereo speaker port.

The CM1454's C-RC filters have a cut-off frequency of 60MHz and an attenuation of better than 35dB over the 800MHz to 2.7GHz frequency range. The C-L-C filters have a cut-off frequency of 21MHz with an attenuation of 40dB at 1Ghz. The parts integrate ESD protection diodes on every pin that provide a very high level of protection for sensitive electronic components against possible electrostatic discharge (ESD). The ESD protection diodes connected to the filter ports are designed and characterized to safely dissipate ESD strikes of ±15kV, which is beyond the requirement of the IEC61000-4-2 international standard. In accordance with MIL-STD-883 (Method 3015) specification for Human Body Model (HBM) ESD, the pins are also protected for contact discharges at greater than ±30kV.

The CM1454 incorporates *OptiGuard*<sup>™</sup> which results in improved reliability at assembly. The CM1454 is available in a space saving, low profile Chip Scale Package with, RoHS compliant, lead-free finishing.

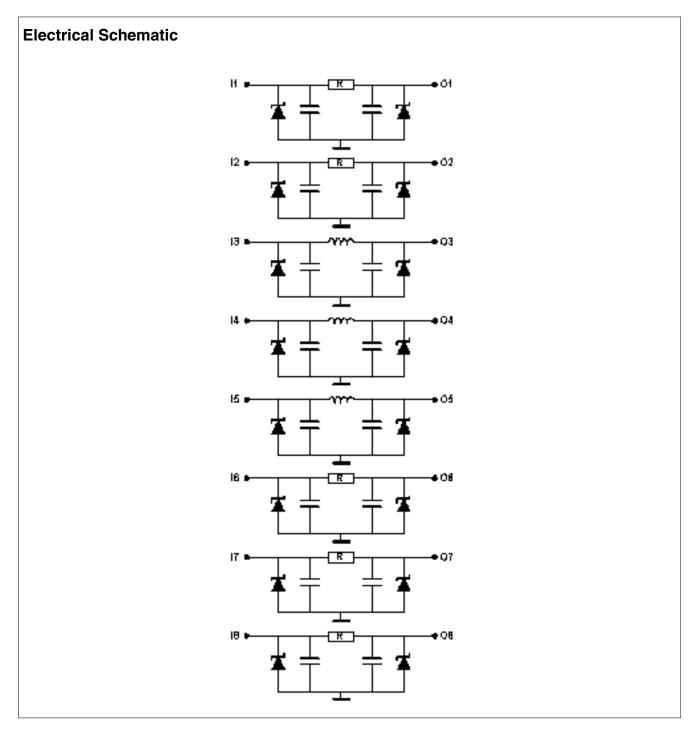
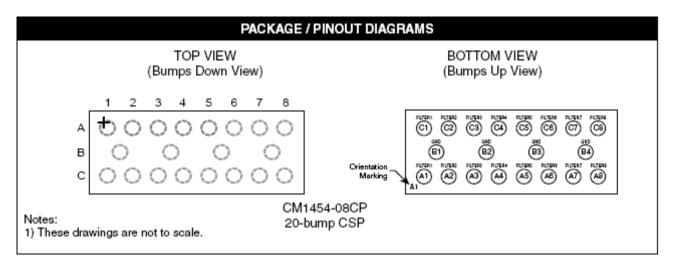


Figure 1. CM1454 Schematic Diagram of R-C and L-C Filter Arrays with ESD



PIN DESCRIPTIONS						
PIN NUMBER	PIN DESCRIPTION	PIN NUMBER	PIN DESCRIPTION			
A1	Filter #1 (Microphone)	B3	GND			
A2	Filter #2 (Microphone)	B4	GND			
A3 Filter #3 (Stereo Headphone)		C1	Filter #1			
A4	A4 Filter #4 (Left Speaker)		Filter #2			
A5	A5 Filter #5 (Right Speaker)		Filter #3			
A6	Filter #6 (Accessory ID)	C4	Filter #4			
A7	Filter #7 (Data)	C5	Filter #5			
A8	Filter #8(Data)	C6	Filter #6			
B1	GND	C7	Filter #7			
B2	GND	C8	Filter #8			

# **Ordering Information**

PART NUMBERING INFORMATION				
		Lead-free Finish		
Bumps	Package	Ordering Part Number <sup>1</sup>	Part Marking	
20	CSP	CM1454-08CP	N548	

Note 1: Parts are shipped in Tape & Reel form unless otherwise specified.

# **Specifications**

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	RATING	UNITS		
Storage Temperature Range	-65 to +150	°C		
DC current per Inductor	30	mA		
DC Package Power Rating	0.5	W		

STANDARD OPERATING CONDITIONS					
PARAMETER	RATING	UNITS			
Operating Temperature Range	-40 to +85	°C			

#### ELECTRICAL OPERATING CHARACTERISTICS (NOTE 1) SYMBOL **PARAMETER** CONDITIONS MIN TYP MAX UNITS R Resistance 80 100 120 Ω C, Capacitance 2.5V dc; 1MHz, 30mV ac 24 30 36 рF L Inductance 3.0 nΗ $R_{\scriptscriptstyle E}$ Equivalent Series Resistance of Inductor 0.25 Ω рF $C_2$ 0V dc; 1MHz, 30mV ac; 100 125 150 Capacitance 2.5V dc; 1MHz, 30mV ac; 64 80 96 рF **Cut-off frequency** MHz $Z_{\text{SOURCE}} = 50\Omega$ , $Z_{\text{LOAD}} = 50\Omega$ Cut-off frequency 21 $\mathsf{MHz}$ $f_{LC}$ $\mathbf{Z}_{\text{SOURCE}} \! = 50\Omega, \, \mathbf{Z}_{\text{LOAD}} \! = 50\Omega$ ٧ $I = 10 \mu A$ $V_{st}$ Stand-off Voltage 6.0 $V_{IN} = 3.3V$ 0.1 1.0 Diode Leakage Current μΑ $I_{\mathsf{LEAK}}$ Signal Clamp Voltage $V_{\text{SIG}}$ Positive Clamp $I_{\tiny LOAD} = 10 mA$ 5.6 6.8 9.0 ٧ **Negative Clamp** $I_{LOAD} = -10mA$ -1.5 -0.8 -0.4 ٧ $\boldsymbol{V}_{\text{ESD}}$ In-system ESD Withstand Voltage Notes 2 and 3 a) Human Body Model, MIL-STD-883, Method ±30 kV b) Contact Discharge per IEC 61000-4-2 Level k۷ ±15 $\boldsymbol{R}_{\scriptscriptstyle DYN}$ Dynamic Resistance Positive 2.3 Ω Negative 0.9 Ω

Note 1: T<sub>A</sub>=25°C unless otherwise specified.

Note 2: ESD applied to input and output pins with respect to GND, one at a time.

Note 3: Unused pins are left open.

## **Performance Information**

Typical Filter Performance (nominal conditions unless specified otherwise, 50 Ohm Environment)

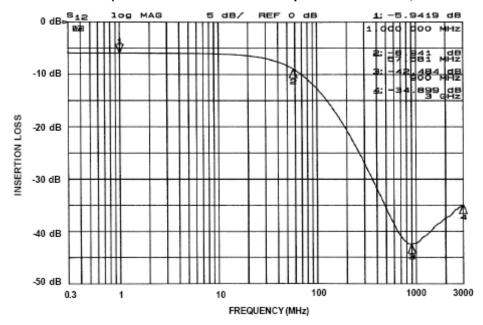


Figure 2. Attenuation Curve for CM1454 RC Filters: 1, 2, 6, 7, and 8

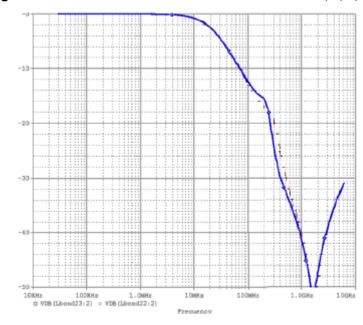


Figure 3. Attenuation Curve for CM1454 RC Filters: 3, 4, and 5

CM1454

## **Diode Characteristics (nominal conditions unless specified otherwise)**

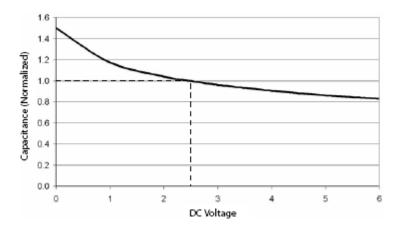


Figure 4. Typical Diode Capacitance VS. Input Voltage (normalized to 2.5VDC)

# **Application Information**

PARAMETER	VALUE
Pad Size on PCB	0.240mm
Pad Shape	Round
Pad Definition	Non-Solder Mask defined pads
Solder Mask Opening	0.290mm Round
Solder Stencil Thickness	0.125mm - 0.150mm
Solder Stencil Aperture Opening (laser cut, 5% tapered walls)	0.300mm Round
Solder Flux Ratio	50/50 by volume
Solder Paste Type	No Clean
Pad Protective Finish	OSP (Entek Cu Plus 106A)
Tolerance — Edge To Corner Ball	<u>+</u> 50μm
Solder Ball Side Coplanarity	<u>+</u> 20μm
Maximum Dwell Time Above Liquidous	60 seconds
Maximum Soldering Temperature for Lead-free Devices using a Lead-free Solder Paste	260°C

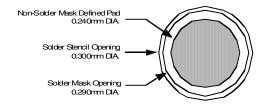


Figure 5. Recommended Non-Solder Mask Defined Pad Illustration

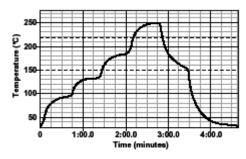
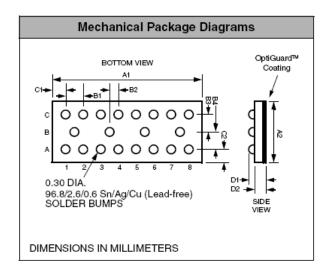


Figure 6. Lead-free (SnAgCu) Solder Ball Reflow Profile

## **Mechanical Specifications**

CM1454 devices are packaged in custom Chip Scale Packages (CSP). See Application Note AP-217 for more information at: <a href="http://www.wlcspforum.org/documents/pdf/ap-217.pdf">http://www.wlcspforum.org/documents/pdf/ap-217.pdf</a>.

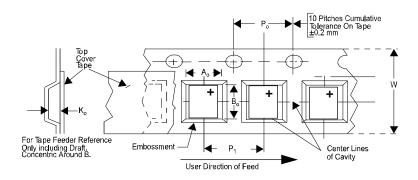
PACKAGE DIMENSIONS							
Package		Custom CSP					
Burr	nps	20					
Dim	M	Millimeters					
	Min	Nom	Max	Min	Nom	Max	
<b>A</b> 1	3.915	3.960	4.005	0.1541	0.1559	0.1577	
A2	1.541	1.586	1.631	0.0607	0.0624	0.0642 0.0199 0.0100	
B1	0.495	0.500	0.505	0.0195	0.0197		
B2	0.245	0.250	0.255	0.0096	0.0098		
В3	0.430	0.435	0.440	0.0169 0.0171		0.0173	
B4	0.430	0.435	0.440	0.0169	0.0173		
C1	0.180	0.230	0.280	0.0071	0.0091	0.0110 0.0161 0.0281	
C2	0.308	0.358	0.408	0.0121	0.0141		
D1	0.574	0.644	0.714	0.0226	0.0254		
D2	0.368	0.419	0.470	0.0145	0.0165	0.0185	
# per tape and reel		3500 pieces					
Controlling dimension: millimeters							



Package Dimensions for CM1454-08CP Chip Scale Package

#### **CSP Tape and Reel Specifications**

PA	ART NUMBER	CHIP SIZE (mm)	POCKET SIZE (mm) B <sub>o</sub> X A <sub>o</sub> X K <sub>o</sub>	TAPE WIDTH W	REEL DIAMETER	QTY PER REEL	P <sub>o</sub>	P,
CI	M1454-08CP	3.96 X 1.586X 0.640	4.06 X 1.98 X 0.76	12mm	330mm (13")	3500	4mm	4mm



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